

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	8	("20010018229" "5206749" "5258320" "5438241" "5739800" "6204079" "6277711" "6283693").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/14 15:51
L3	288	transfer\$4 near3 (film membrane) with stretch\$4	USPAT; EPO; JPO	OR	ON	2005/04/14 16:01
L4	0	"361"/\$.ccls. and 2	USPAT; EPO; JPO	OR	ON	2005/04/14 15:55
L5	5	"438"/\$.ccls. and 2	USPAT; EPO; JPO	OR	ON	2005/04/14 15:55
L6	1	"29"/\$.ccls. and 2	USPAT; EPO; JPO	OR	ON	2005/04/14 15:55
L7	36	transfer\$4 near3 (plate substrate film membrane layer) with stretch\$4 and array and (circuit chip wafer)	USPAT; EPO; JPO	OR	ON	2005/04/14 16:24
L8	13	("3786307" "3845615" "4112333" "4229736" "4233602" "4359729" "4368467" "4368523" "4429305" "4448491" "4491391" "4499459" "4586039").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/14 16:06
L9	32	("4808983").URPN.	USPAT	OR	ON	2005/04/14 16:07
L10	51	"438"/\$.ccls. and (mov\$4 transfer\$4) near3 (plate substrate film membrane layer) with (expand\$4 stretch\$4) and array and (circuit chip wafer)	USPAT; EPO; JPO	OR	ON	2005/04/14 16:32
L11	12	"29"/\$.ccls. and (mov\$4 transfer\$4) near3 (plate substrate film membrane layer) with (expand\$4 stretch\$4) and array and (circuit chip wafer)	USPAT; EPO; JPO	OR	ON	2005/04/14 16:33
L12	39	"29"/\$ and (mov\$4 transfer\$4) near3 (plate substrate film membrane layer) with (expand\$4 stretch\$4) and array and (circuit chip wafer)	USPAT; EPO; JPO	OR	ON	2005/04/14 16:35
L13	22	"359"/\$ and (mov\$4 transfer\$4) near3 (plate substrate film membrane layer) with (expand\$4 stretch\$4) and array and (circuit chip wafer)	USPAT; EPO; JPO	OR	ON	2005/04/14 16:36
L14	4	"269"/\$ and (mov\$4 transfer\$4) near3 (plate substrate film membrane layer) with (expand\$4 stretch\$4) and array and (circuit chip wafer)	USPAT; EPO; JPO	OR	ON	2005/04/14 16:36

L15	44	"257"/\$ and (mov\$4 transfer\$4) near3 (plate substrate film membrane layer) with (expand\$4 stretch\$4) and array and (circuit chip wafer)	USPAT; EPO; JPO	OR	ON	2005/04/14 16:39
L16	76	"156"/\$ and (mov\$4 transfer\$4) near3 (plate substrate film membrane layer) with (expand\$4 stretch\$4) and array and (circuit chip wafer)	USPAT; EPO; JPO	OR	ON	2005/04/14 16:39